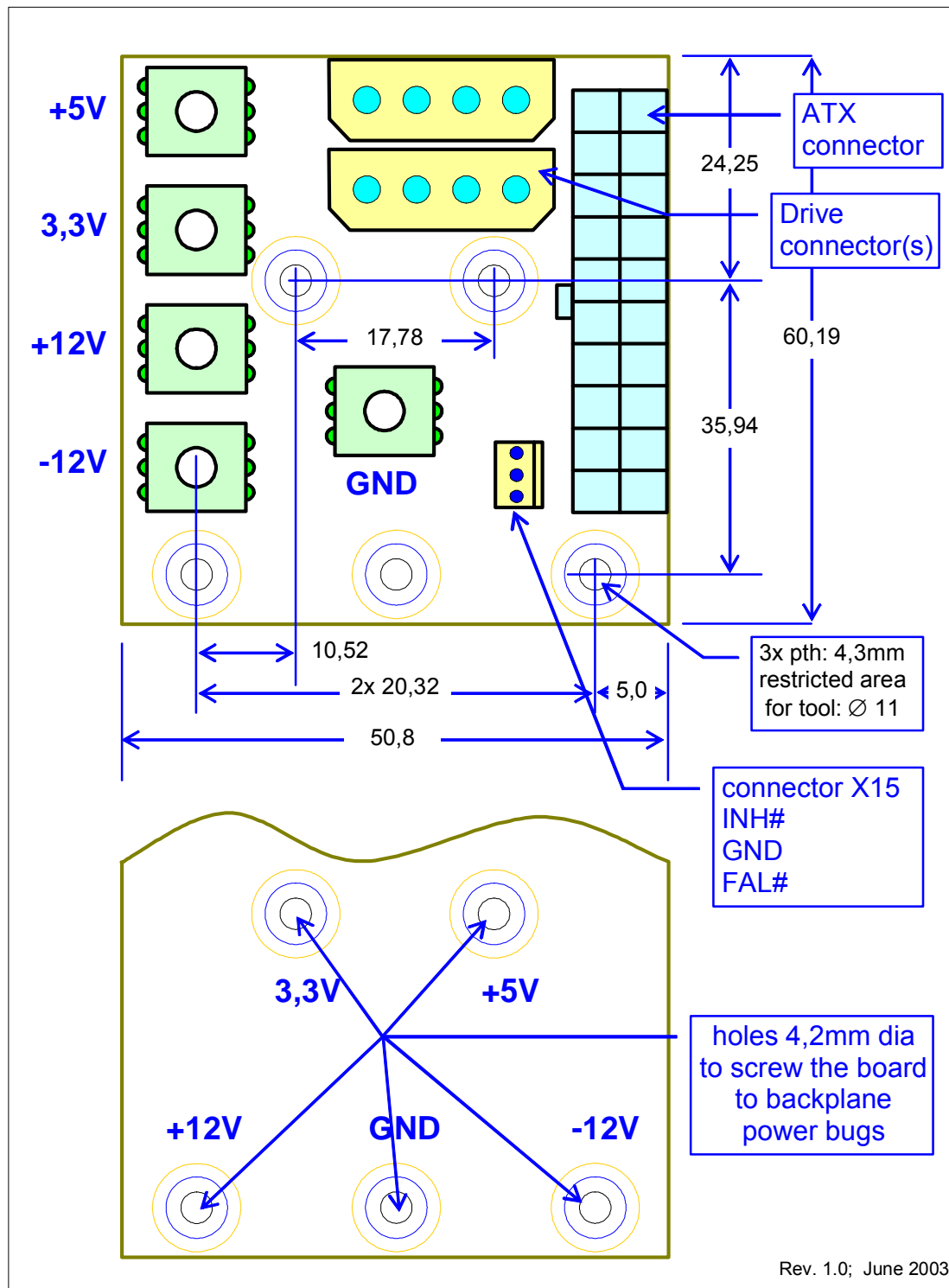


## Power Piggyback 23098-100

short form Manual (Rev 2.0)

The Power Piggyback board can be used if ATX PSU's shall be connected to a Backplane. In this case the signal "INH#" at connector X15 shall be jumpered to GND that the ATX-PSU power up.

Connector X15 is jumpered for ATX PSU's; if you use other PSU's like CPCI PSU's (acc. to PICMG 2.11) than please remove the jumper. For more explanation see next page.



**ATX Power Connector (X3, X4)**

3,3V	11	1	3,3V
- 12V	12	2	3,3V
GND	13	3	GND
INH#	14	4	+5V
GND	15	5	GND
GND	16	6	+5V
GND	17	7	GND
nc	18	8	FAL#
+5V	19	9	nc
+5V	20	10	+12V

pinout: top view on connector

free connector: Molex # 39-01-2205

crimp terminal: Molex # 39-00-0039  
(AWG 18-24, Bag)

CPCI Signal INH# uses a pin defined within the  
ATX spec as PS-ON;

both (INH#, PS-ON) used to drive the PSU  
ON/OFF;

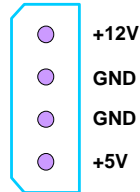
Logic Level is reversed; to drive PSU on, drive

INH#: HIGH (PCMG 2.11 PSU's)

PS-ON: LOW (ATX PSU's)

**Disk Drive  
Power Connector (X5, X6)**

top view on connector



board connector: Molex # 15-24-4049  
free connector: Molex # 15-24-3053  
(IDC, AWG 16)